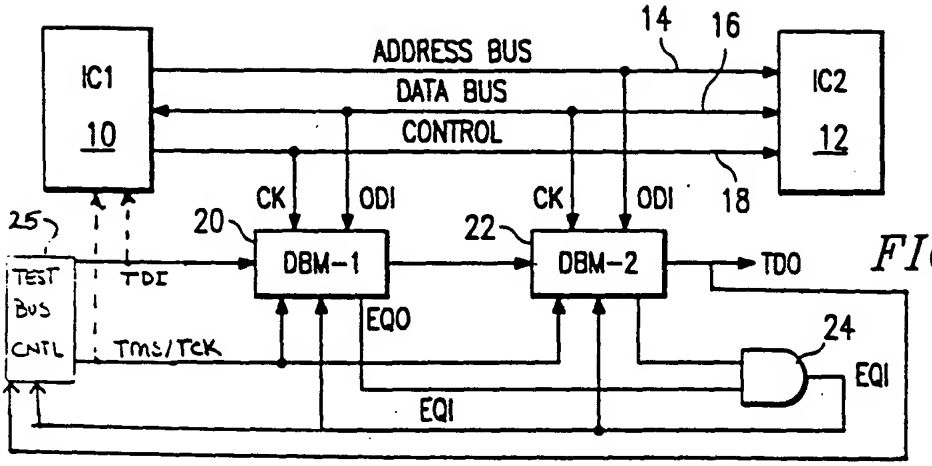

IC1
10
ADDRESS BUS
DATA BUS
CONTROL
14
16
IC2
12
18
CK
ODI
CK
ODI
25
20
22
TEST
BUS
CNTL
TDI
DBM-1
DBM-2
TDO
EQO
TMS/TCK
24
EQI
EQI

FIG. 1

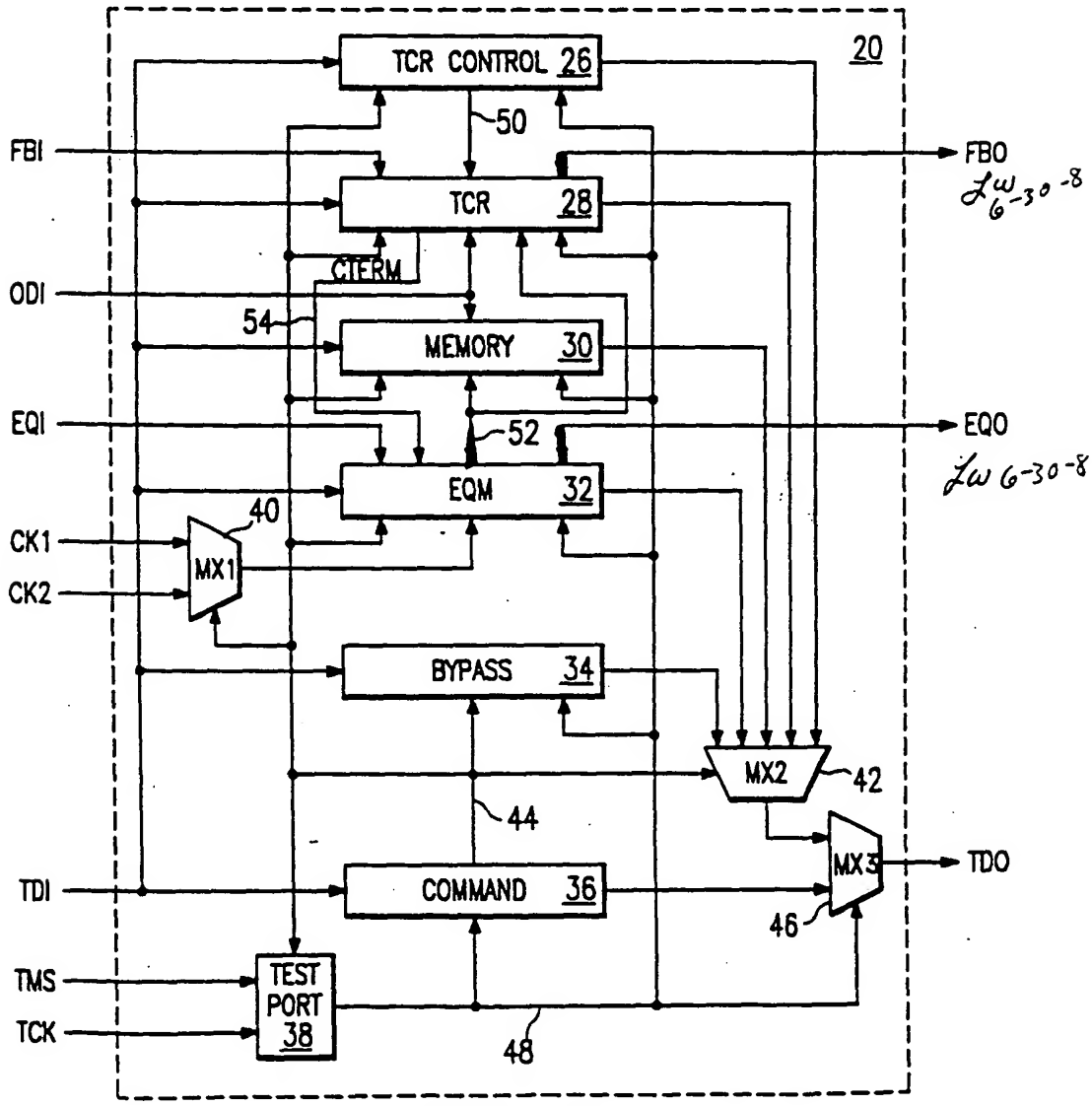

20
TCR CONTROL 26
50
FBI
FBO
TCR 28
CTERM
ODI
54
MEMORY 30
EQI
52
EQO
EQM 32
40
CK1
CK2
MX1
BYPASS 34
MX2
42
44
MX3
TDO
TDI
COMMAND 36
46
TMS
TCK
TEST
PORT
38
48

FIG. 2

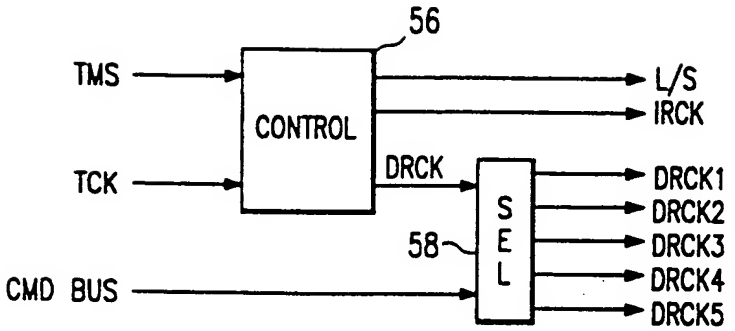


FIG. 3

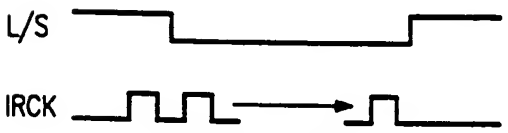


FIG. 3a

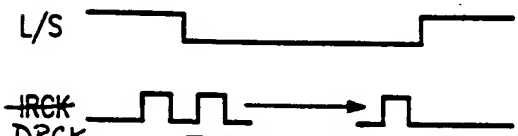


FIG. 3b

LW
6-30-89

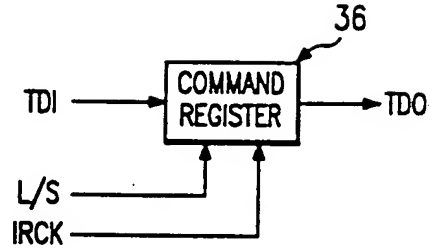


FIG. 4

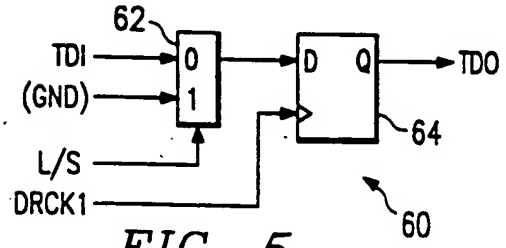


FIG. 5

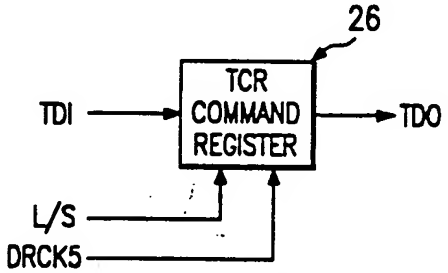


FIG. 6

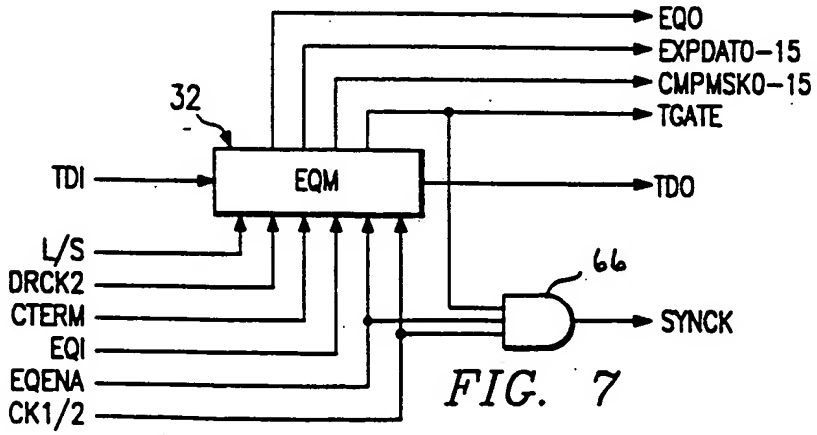


FIG. 7

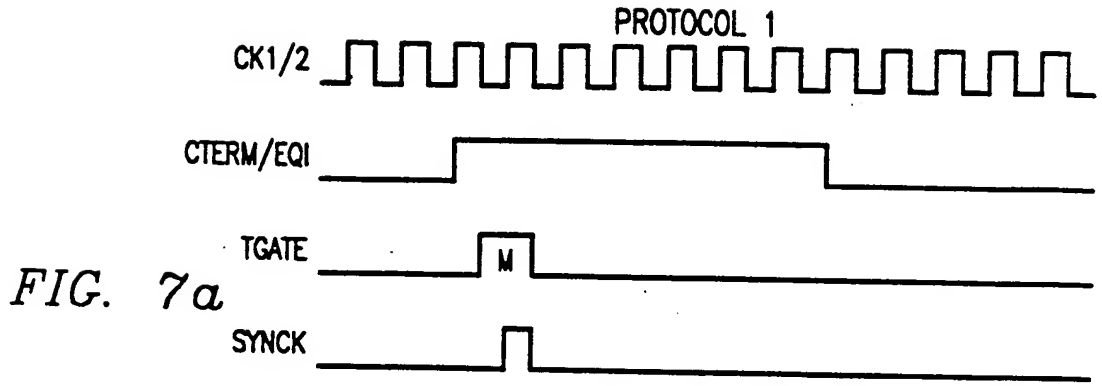


FIG. 7a

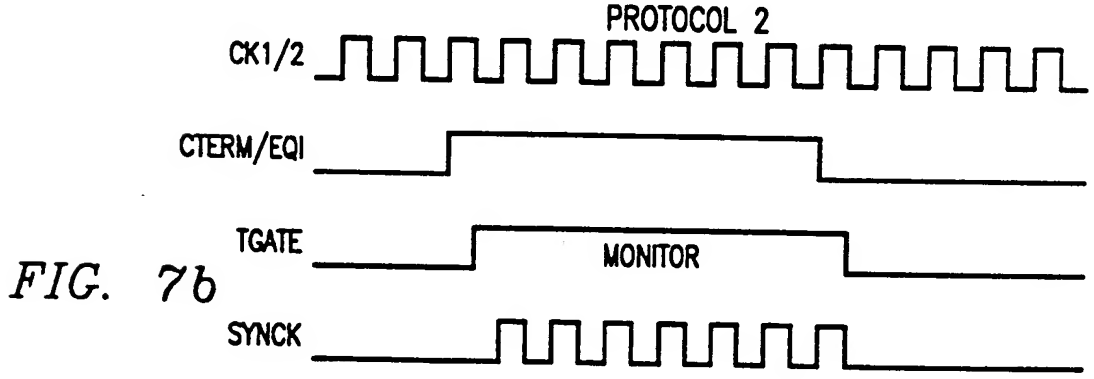


FIG. 7b

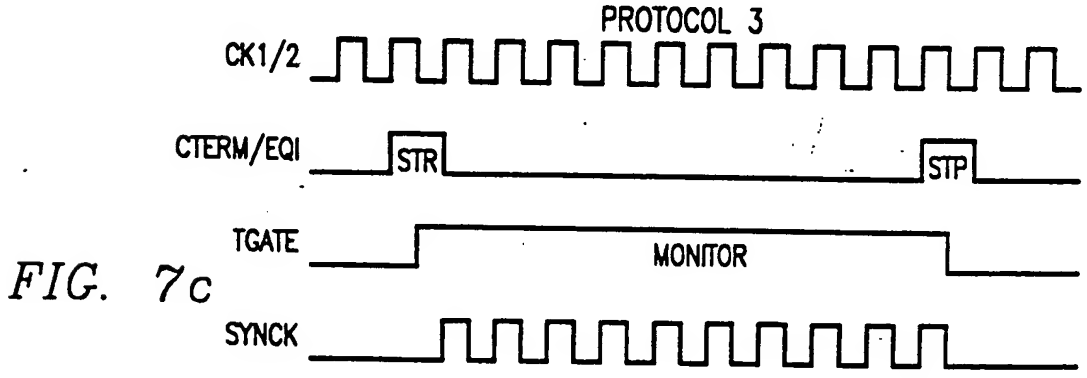


FIG. 7c

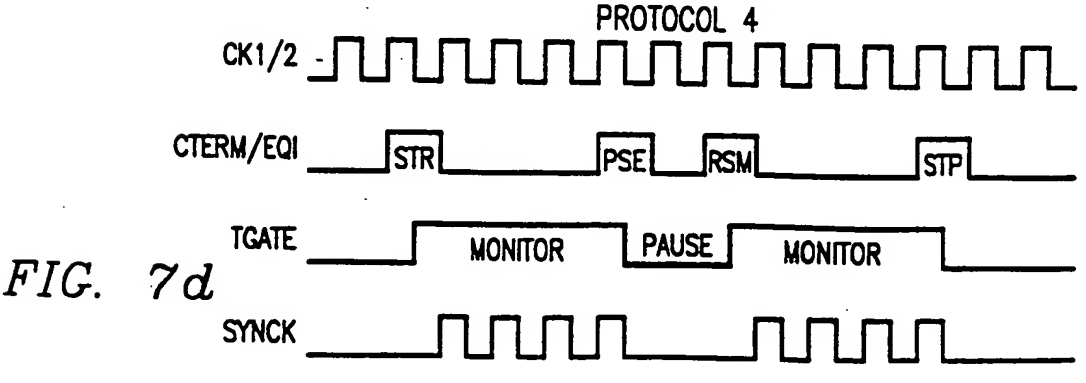


FIG. 7d

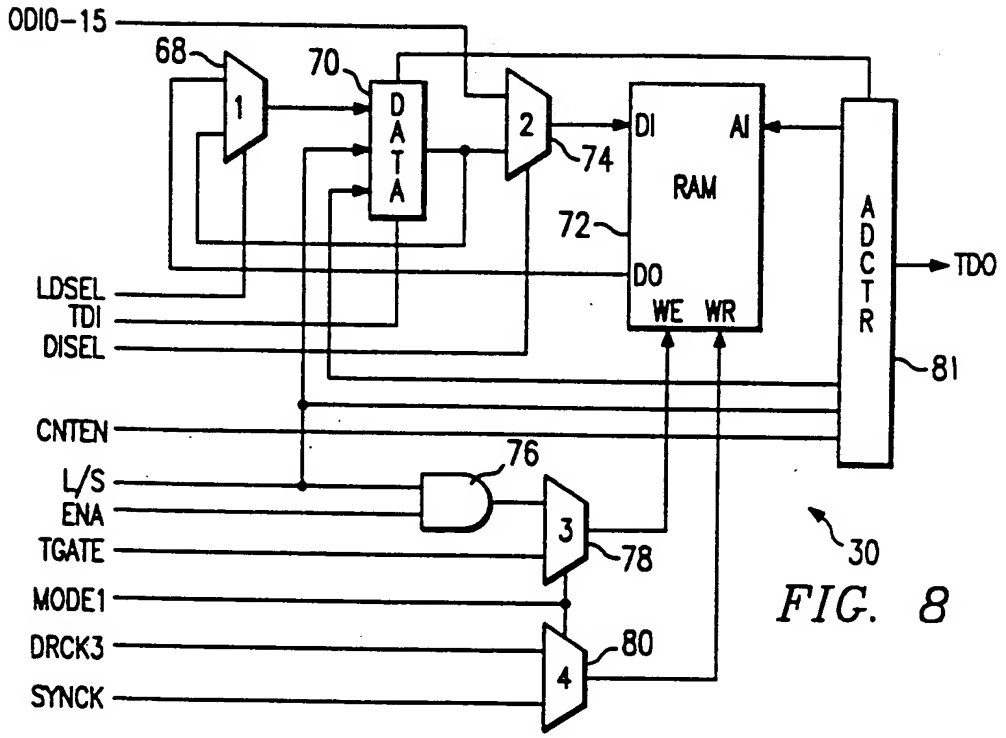


FIG. 8

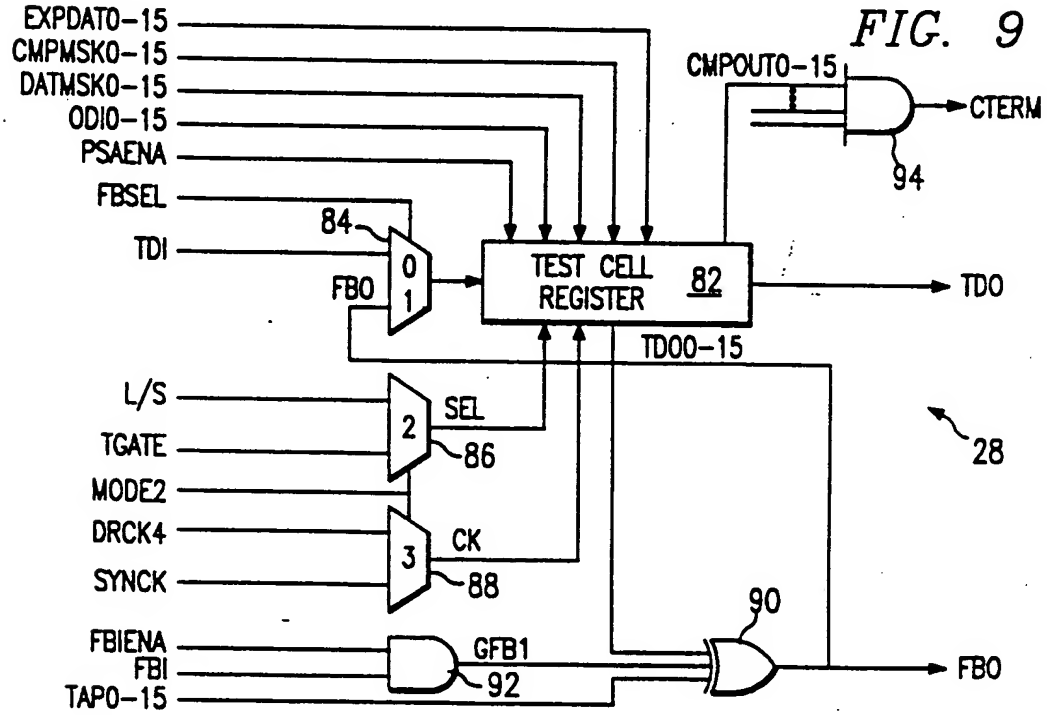


FIG. 9

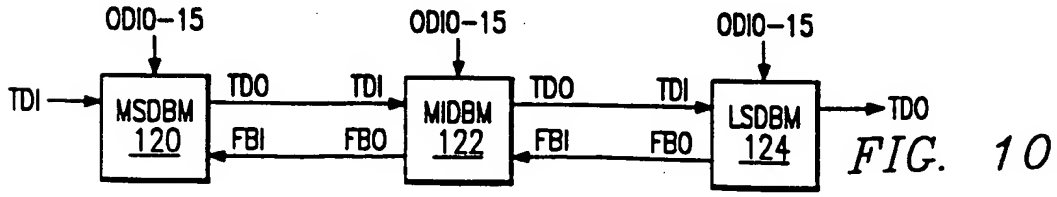


FIG. 10

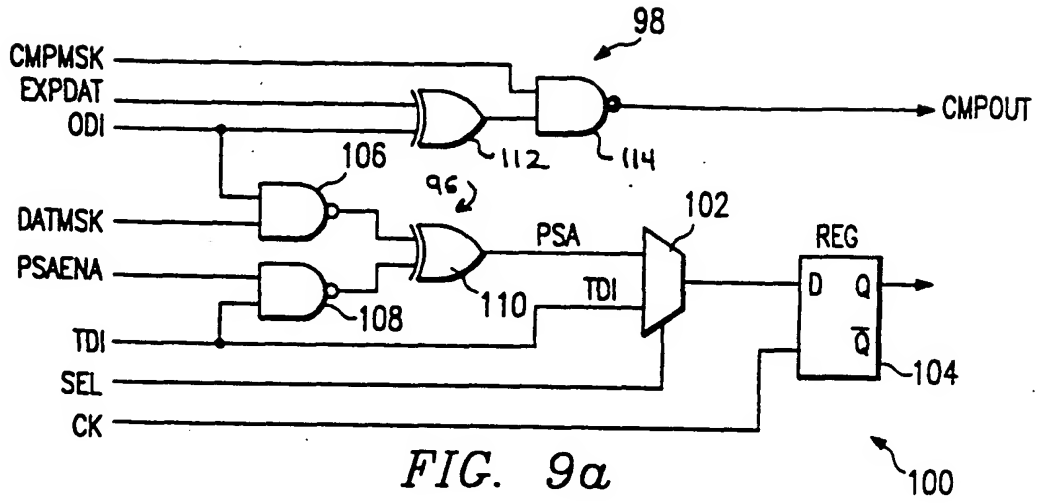

98
CMPMSK
EXPDAT
ODI
CMPOUT
112
114
106
96
DATMSK
PSAENA
PSA
102
REG
D
Q
TDI
110
108
TDI
SEL
CK
104
100

FIG. 9a

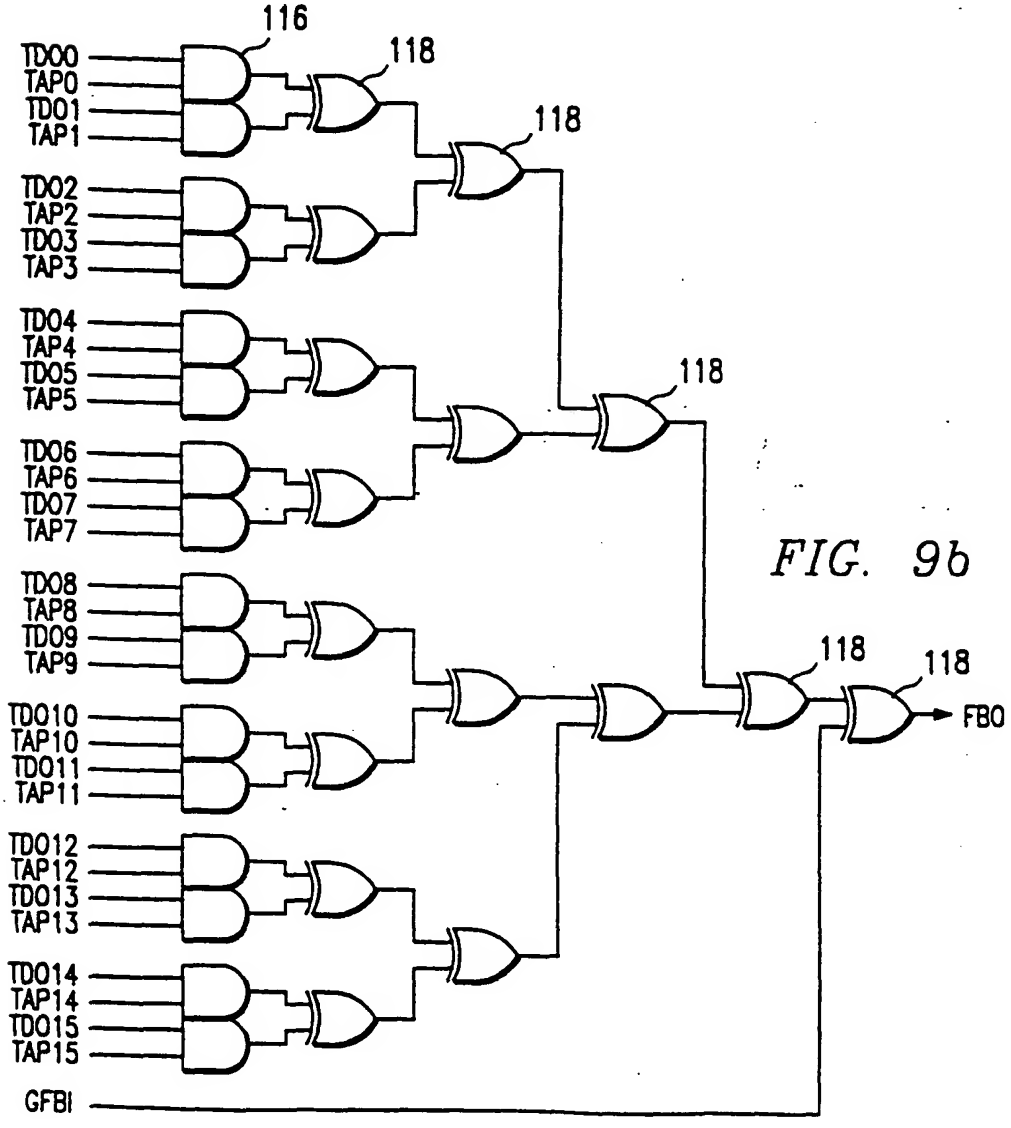
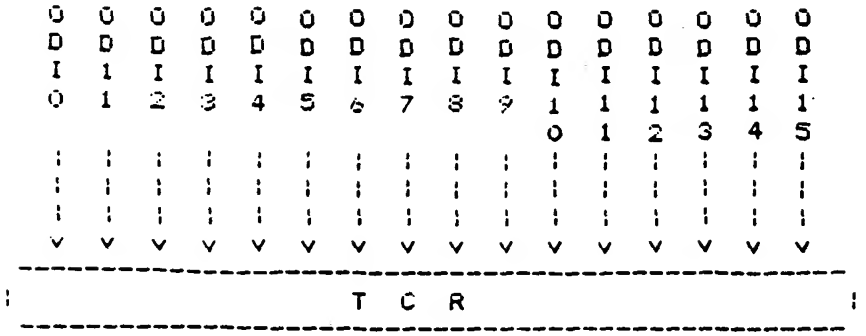
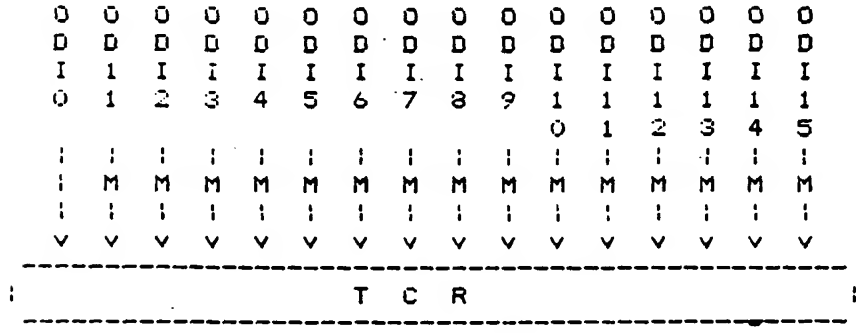

116
118
TDO0
TAP0
TDO1
TAP1
TDO2
TAP2
TDO3
TAP3
TDO4
TAP4
TDO5
TAP5
TDO6
TAP6
TDO7
TAP7
TDO8
TAP8
TDO9
TAP9
TDO10
TAP10
TDO11
TAP11
TDO12
TAP12
TDO13
TAP13
TDO14
TAP14
TDO15
TAP15
GFBI
FBO

FIG. 9b

igure 9c - Performing Parallel Signature Analysis on all ODI inputs



igure 9d - Performing Single Signature Analysis on one ODI input